

ELC 5396: Digital Communications

Liang Dong

Communication Systems

Digital Com munication

Block Diagram of Digital Com munication System

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Communication Systems

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Communication Systems

Digital Com munication

Block Diagram of Digital Com munication System Communication systems send information electronically over communication channels
Provide for electronic exchange of multimedia data
Voice, data, video, music, email, web pages, etc.



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Digital Com munication

Block Diagram of Digital Com munication System Communication systems send information electronically over communication channels
Provide for electronic exchange of multimedia data
Voice, data, video, music, email, web pages, etc.

 Communication systems recreate transmitted information at receiver with high fidelity
Design challenges include hardware, system, and network issues



Communication Systems

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Block Diagram of Digital Com munication System

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 Provide for electronic exchange of multimedia data
 Voice, data, video, music, email, web pages, etc.
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 Design challenges include hardware, system, and network issues

Communication Systems Today

Radio and TV broadcasting Public Switched Telephone Network (voice,fax,modem) Cellular Phones Computer networks (LANs, WANs, and the Internet) Satellite systems (pagers, voice/data, movie broadcasts) WiFi and Bluetooth



Device Challenges

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Block Diagram of Digital Com munication System

- Analog and RF Components
- A/D Converters
- Size, Power, Cost
- Multiple Antennas
- Multiradio Coexistance





Design Challenges

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Block Diagram of Digital Com munication System

Hardware Design

Precise components Small, lightweight, low power Cheap

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High frequency operation



Design Challenges

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Hardware Design

Precise components Small, lightweight, low power Cheap

High frequency operation

System Design

Converting and transferring information High data rates Robust to noise and interference Supports many users



Design Challenges

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System Design

Converting and transferring information High data rates Robust to noise and interference Supports many users

Network Design

Connectivity and high speed Energy and delay constraints



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Block Diagram of Digital Com munication System

1 Error correction/detection



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Block Diagram of Digital Com munication System

- **1** Error correction/detection
- **2** Better encryption algorithms: Can not be done in analog communication



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1 Error correction/detection

Better encryption algorithms: Can not be done in analog communication

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3 More reliable data processing



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Block Diagram of Digital Com munication System

1 Error correction/detection

Better encryption algorithms: Can not be done in analog communication

- 3 More reliable data processing
- 4 Easily reproducible designs



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1 Error correction/detection

2 Better encryption algorithms: Can not be done in analog communication

- 3 More reliable data processing
- 4 Easily reproducible designs
- 5 Reduced cost



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1 Error correction/detection

Better encryption algorithms: Can not be done in analog communication

- 3 More reliable data processing
- 4 Easily reproducible designs
- 5 Reduced cost
- 6 Easier data multiplexing



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Better encryption algorithms: Can not be done in analog communication

- 3 More reliable data processing
- 4 Easily reproducible designs
- 5 Reduced cost
- 6 Easier data multiplexing
- 7 Facilitate data compression



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Block Diagram of Digital Con munication System 1 Heavy signal processing



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Block Diagram of Digital Com munication System 1 Heavy signal processing

2 Synchronization is crucial



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Block Diagram of Digital Com munication System

- 1 Heavy signal processing
- 2 Synchronization is crucial
- **3** Larger transmission bandwidth



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Block Diagram of Digital Com munication System

- 1 Heavy signal processing
- Synchronization is crucial
- 3 Larger transmission bandwidth
- 4 Non-graceful degradation

B	Goals in Communication System Design
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Communication Systems Digital Com- nunication	1 To maximize transmission rate, <i>R</i>
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Block Diagram of Digital Com munication System

- **1** To maximize transmission rate, R
- **2** To maximize system utilization, U



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Block Diagram of Digital Com munication System

- **1** To maximize transmission rate, R
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3 To minimize bit error rate, P_e



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Block Diagram of Digital Com munication System

- **1** To maximize transmission rate, R
- 2 To maximize system utilization, U
- **3** To minimize bit error rate, *P*_e
- 4 To minimize required systems bandwidth, W



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- 4 To minimize required systems bandwidth, W

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5 To minimize system complexity, C_x



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- Block Diagram of Digital Com munication System

- **1** To maximize transmission rate, R
- 2 To maximize system utilization, U
- 3 To minimize bit error rate, P_e
- 4 To minimize required systems bandwidth, W

- 5 To minimize system complexity, C_x
- 6 To minimize required power, E_b/N_0

B

Block Diagram of Digital Communication System



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Information Source and Sink

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Block Diagram of Digital Communication System

0's.

 Information Source and Input Transducer: The source of information can be analog or digital, Analog: audio or video signal, Digital: like teletype signal.
In digital communication the signal produced by this

source is converted into digital signal consists of 1's and

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Information Source and Sink

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Block Diagram of Digital Communication System Information Source and Input Transducer: The source of information can be analog or digital, Analog: audio or video signal, Digital: like teletype signal.
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Output Transducer:

0's.

The signal in desired format analog or digital at the output

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Channel

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Channel:

The communication channel is the physical medium that is used for transmitting signals from transmitter to receiver Wireless channels: Wireless Systems Wired channels: Telephony

Channel discrimination on the basis of their property and characteristics, like AWGN channel etc.



Source Encoder and Decoder

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Block Diagram of Digital Communication System

Source Encoder

In digital communication we convert the signal from source into digital signal.

The point to remember is we should like to use as few binary digits as possible to represent the signal. In such a way this efficient representation of the source output results in little or no redundancy. This sequence of binary digits is called information sequence.

Source Encoding or Data Compression: the process of efficiently converting the output of analog or digital source into a sequence of binary digits is known as source encoding.



Source Encoder and Decoder

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Source Decoder

Source decoder tries to decode the sequence from the knowledge of the encoding algorithm, which results in the approximate replica of the input at the transmitter end.



Channel Encoder and Decoder

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Channel Encoder:

The information sequence is passed through the channel encoder.

The purpose of the channel encoder is to introduce, in controlled manner, some redundancy in the binary information sequence that can be used at the receiver to overcome the effects of noise and interference encountered in the transmission on the signal through the channel.

e.g. take k bits of the information sequence and map that k bits to unique n bit sequence called code word. The amount of redundancy introduced is measured by the ratio n/k and the reciprocal of this ratio (k/n) is known as rate of code or code rate.



Channel Encoder and Decoder

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Channel Decoder:

Channel decoder attempts to reconstruct the original information sequence from the knowledge of the code used by the channel encoder and the redundancy contained in the received data.



Digital Modulator and Demodulator

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Digital Modulator:

The binary sequence is passed to digital modulator which in turns convert the sequence into electric signals so that we can transmit them on channel.

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The digital modulator maps the binary sequences into signal wave forms.



Digital Modulator and Demodulator

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Digital Modulator:

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Digital Demodulator:

The digital demodulator processes the channel corrupted transmitted waveform and reduces the waveform to the sequence of numbers that represents estimates of the transmitted data symbols.



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Block Diagram of Digital Communication System The source coding algorithm plays an important role in higher code rate



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Block Diagram of Digital Communication System The source coding algorithm plays an important role in higher code rate

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The channel encoder introduces redundancy in data



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Block Diagram of Digital Communication System

- The source coding algorithm plays an important role in higher code rate
- The channel encoder introduces redundancy in data
- The modulation scheme plays an important role in deciding the data rate and immunity of signal towards the errors introduced by the channel



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Block Diagram of Digital Communication System

- The source coding algorithm plays an important role in higher code rate
- The channel encoder introduces redundancy in data
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 Channel can introduce many types of errors due to thermal noise, interference, etc.



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Block Diagram of Digital Communication System

- The source coding algorithm plays an important role in higher code rate
- The channel encoder introduces redundancy in data
- The modulation scheme plays an important role in deciding the data rate and immunity of signal towards the errors introduced by the channel
- Channel can introduce many types of errors due to thermal noise, interference, etc.
- The demodulator and decoder should provide low Bit Error Rate (BER).

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