Name_______________________________________________

ELC 3314 – Electronic Design
Fall 2013
Test 2 – November 14, 2013
Closed Book/Closed Notes
1 hour and 15 minutes

1. The exam is closed-book/closed-notes.

2. A calculator may be used to assist with the test. No laptops or PDAs are allowed. No cellular phones may be used in any way during the test. Unauthorized electronic device use will result in disqualification.

3. You must circle or box your answers to get full credit.

4. All work and steps toward a solution must be clearly shown to obtain credit.

5. Partial credit may be given provided that the grader can clearly follow your work to the extent that an understanding of the problem is demonstrated.

6. No collaboration is allowed on this examination. Only Dr. Baylis or the teaching assistant may be consulted for clarification.

7. You may attach extra sheets to the exam if necessary. Each page should contain your name, the problem number, and the page number for that problem.

Please sign the statement below. YOU MUST SIGN THE STATEMENT OR YOU WILL GET A ZERO FOR THIS EXAMINATION!!!

I hereby testify that I have neither provided or received information from unauthorized sources during the test and that this test is the sole product of my effort.

Signed ________________________________________                          Date____________________
**PROBLEM 1 (20 points):** Use a large-signal model for the transistor in the appropriate mode (active, cutoff, or saturation) with DC analysis to find $I_C$ and $V_{CE}$ for the following circuit. Assume $\beta = 200$. 

(More workspace on next page)
(More workspace for Problem 1):
PROBLEM 2 (20 points): Consider the common-emitter amplifier shown below. Assume $\beta = 100$, $V_{BEQ} = 0.7 \text{ V}$, and $V_T = 26 \times 10^{-3} \text{ V}$. Also assume that the capacitance values are very large for all capacitors.

(a) (6 points) What is the value of $r_\pi$ in the small-signal transistor model?
(b) (7 points) Find the input resistance $Z_{in}$ for this amplifier.

(c) (7 points) Find the value of the voltage gain

$$A_V = \frac{v_o}{v_{in}}$$

for this amplifier.
PROBLEM 3 (20 points): Consider the MOSFET amplifier below. The MOSFET has $V_t = 1 \text{ V}$, $\lambda = 0$, and $K = 0.5 \text{ mA/V}^2$. Verify that your assumed region of operation is correct.

(a) (10 points) Find $V_{GSQ}$, $I_{DSQ}$, and $V_{DSQ}$.

(Problem 3 continued on next page)
(b) (10 points) Find the voltage gain

\[ A_v = \frac{v_o}{v_{in}} \]
**PROBLEM 4 (20 points):** Choose the value of $R$ necessary to provide $I_{C2} = 3$ mA if $Q_1$, $Q_2$, and $Q_3$ are identical. Do not ignore base currents. Use $\beta = 100$ and assume that $V_{BE} = 0.6$ V (roughly) for the purpose of circuit analysis.
PROBLEM 5 (20 points): For the differential amplifier pictured below, find the small-signal voltage gain $A_v = v_o/v_{id}$, where $v_{id} = v_i1 - v_i2$. You will need to decide whether the gain, as defined, is for a single-ended or balanced load and use the appropriate formula(s). Assume $\beta = 100$ and $V_T = 26 \times 10^{-3}$ V for the identical transistors $Q_1$ and $Q_2$, assume that both transistors are biased in the active region, and assume that the output resistance of the DC current source is infinite. Do not neglect the quiescent bias base currents.

Equations:

$A_{vds}$(single-ended) = $v_o2/v_{id} = R_c\beta/2(r_n + (\beta + 1)R_EF)$, $A_{vdb}$(balanced) = $R_c\beta/r_n + (\beta + 1)R_EF$, $r_n = \beta V_T/I_CQ$

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(more workspace for Problem 5):