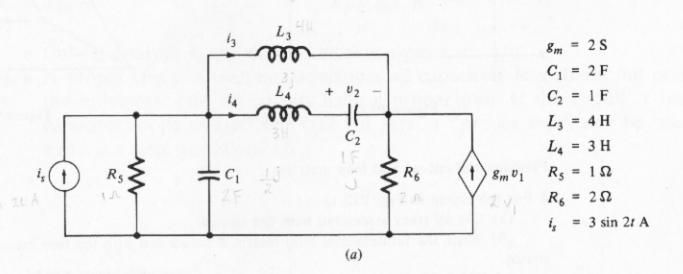


Figure P12.6

The linear time-invariant circuit of Fig. P12.7a, having a digraph shown in Fig. P12.7b, is in the sinusoidal steady state. From the digraph a tree is picked as shown in Fig. P12.7c.

- (a) Write the fundamental cut-set matrix Q.
- (b) Calculate the cut-set admittance matrix \mathbf{Y}_q
- (c) Write the cut-set equations in the frequency domain in terms of the cut-set voltage and the current source:

$$\mathbf{Y}_{q}\mathbf{V}_{t}=\mathbf{I}_{sq}$$



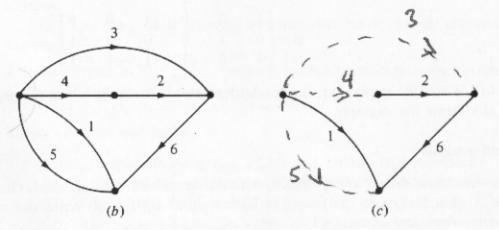


Figure P12.7